

U.S. PTO
10/03/01
12/20/01

FORM PTO-1449 (SUBSTITUTE)

U.S. DEPARTMENT OF COMMERCE
PATENT AND TRADEMARK OFFICEINFORMATION DISCLOSURE
STATEMENT BY APPLICANT
(37 CFR 1.98(b))Attorney Docket No.: W&B-INF-952
Appl. No.

Applicant ROBERT KAISER ET AL.

Filing Date December 20, 2001
Group Art Unit 2133

EXAMINER INITIALS		PATENT NO.	DATE	PATENTEE	CLASS	SUB CLASS	FILING DATE
<i>JP</i>	A	5,313,424	5/17/94	Adams et al.	—	—	
	B						
	C						
	D						
	E						
	F						
	G						
	H						
	I						

FOREIGN PATENT DOCUMENT

		DOCUMENT NO.	DATE	COUNTRY	CLASS	SUB CLASS	TRANSL. YES NO
<i>JP</i>	J	DE 197 08 965 A1	9/24/98	Germany	—	—	
	K						
	L						
	M						
	N						

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)

<i>JP</i>		Tarr M. et al.: "Defect Analysis System Speeds Test and Repair of Redundant Memories", Electronics, January 12, 1984, pp. 175, 177, 179;

EXAMINER

DATE CONSIDERED

John Smith 6/8/04

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.